

CS315-01 Lab Processor Design JAL JALR

Lab 06 Part 1

addi (li.)

add

unimp

Lab 06 Part 2

jal jalr

call ret

jal jump and link J-type

update ra(rd) to PC+4

update PC to PC+imm

jalr (ret) jump to register value and link

update ra(rd) to PC+4 but rd is 0 for ret

update PC to rs1+imm

↑ but imm is 0 for ret

